

Drawing Amendments

The attached sheets of drawings include clearer formal line drawings for all Figs 1-2.

This sheet which includes all Figs. 1-2, replaces the original sheets including all Figs. 1-2.

Please approve the attached formal drawings marked "Replacement Sheet" of all amended Figs. 1-2 are enclosed.

Attachments: Replacement Sheet

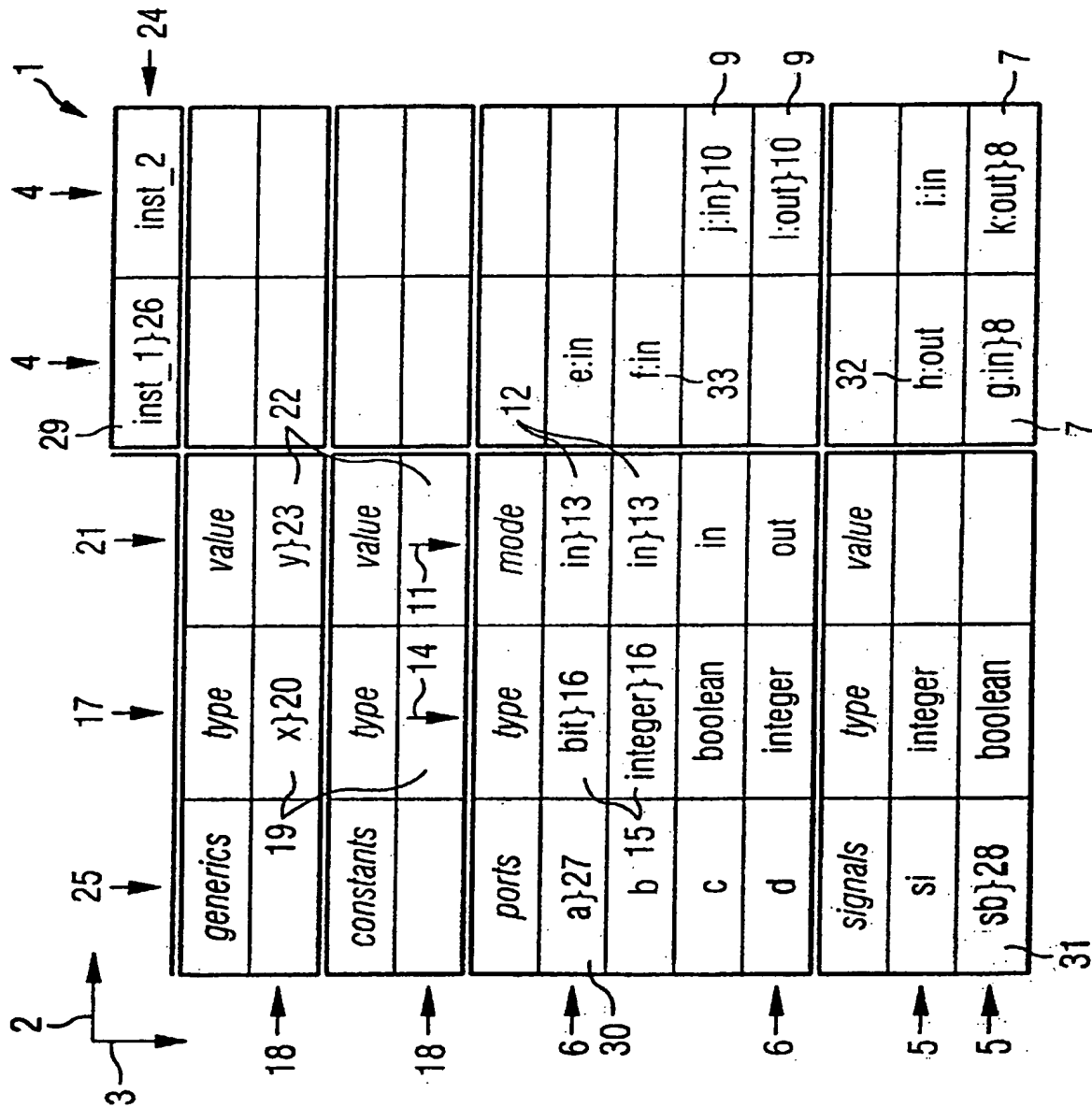




FIG 2

		25	17	21	29	4	4	24
		identifier	mode	type	value	inst_1	inst_2	p0
generics	18	y}38	in	integer}20	3}23		z}40	
		37						
constants	18	ci}38		integer}20	y+5}35			ci}40
	18	---		integer	4		x}40	
ports (entity)	6	a}27	in}13 12	bit}16				
	30	b	in}13 12	integer				
		c	in	boolean	true}36		j, in, bit, boolean2bit}42	
	6	d	out	integer			l, out}10	
signals	5	si_1		integer				si_1, in}41
	5	si_2		integer				si_1+ci}43
	31	sb}28		bit	'1'}36		k, out}8	